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- 57%** Circuits and systems
- 53%** Computing and graphics
- 51%** Power and energy equipment
- 49%** CAD/CAM/CAE systems
- 49%** Internet applications
- 47%** Embedded systems
- 45%** Control technologies
- 45%** IC/semiconductors
- 41%** Scientific computing
- 41%** Business/ERP software
- 40%** Power solutions
- 38%** Security systems and solutions
- 36%** Programmable logic (FPGAs)



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- Computer Design and Test Engineering Reference and Textbooks
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2016 EDITORIAL CALENDAR

Departments: Standards; The Road Ahead; Book Reviews; Conference Reports; CEDA Currents; Design Automation TC Newsletter; Test Technology TC Newsletter; The Last Byte; Roundtables

| ISSUE | CLOSE | MATERIALS | 2016 BONUS DISTRIBUTION |
|------------|--------------------|--------------------|--|
| FEB | 22 DEC 2015 | 28 DEC 2015 | • DATE, 14-18 Mar., Dresden, Germany |
| APR | 25 JAN | 28 JAN | • 2016 VLSI-DAT, 25-27 Apr., Hsinchu, Taiwan • European Test Symposium, 24-29 April, Amsterdam, The Netherlands |
| JUN | 28 MAR | 31 MAR | • ISCAS, 22-25 May, Montreal, QC, Canada • DAC, 5-9 June, San Francisco, CA |
| AUG | 24 MAY | 27 MAY | |
| OCT | 27 JUL | 1 AUG | • ES Week, 1-7 Oct., Pittsburgh, PA • ITC, 15-17 Nov., Fort Worth, TX |
| DEC | 27 SEP | 30 SEP | • ICCAD, TBD |

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